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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

JAPAN P2001-001418 01/09/2001

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 34	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 3
Verified and Acknowledged Examiner's Signature	Initials				

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TITLE

Semiconductor device having impurity region under isolation region

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